

TITLE OF THE INVENTION

EXPOSURE APPARATUS INCLUDING INTERFEROMETER SYSTEM

5 FIELD OF THE INVENTION

10 The present invention relates to an exposure apparatus which includes an interferometer system which measures the position and displacement of an object in the vertical direction and is used to manufacture a liquid crystal board or semiconductor device.

BACKGROUND OF THE INVENTION

15 Figs. 13 and 14 show an example of a measuring means for an alignment apparatus used in a conventional semiconductor exposure apparatus or the like. Fig. 13 is a perspective view showing the arrangement of a measuring system using a laser interferometer. Fig. 14 is a front view showing the overall conventional semiconductor exposure apparatus having the measuring system.

20 In Fig. 14, reference numeral 7 denotes an illumination unit for illuminating a reticle pattern; 8, a reticle having a pattern to be transferred; 9, a projection lens for projecting a reticle pattern onto a wafer; 10, a lens barrel support which supports the projection lens 9; 11, a stage surface plate; 13, an X stage; 14, a Y stage; 15, an X stage driving guide/X

linear motor; and 16, a Y stage driving guide/Y linear motor. A top stage 2 for precise alignment is mounted on the X and Y stages. The top stage 2 moves a long stroke in the X- and Y-axis directions by the guides 15 and 16 and an actuator. Further, the top stage 2 moves a short stroke in the Z-axis direction and rotational directions ωX , ωY , and θ by a Z actuator 17 which drives the top stage 2 with respect to the X stage 13.

In Fig. 13, reference numeral 1 denotes a wafer chuck which supports a wafer (not shown). The top stage 2 constitutes a stage which supports the wafer chuck 1. Reference numeral 3 denotes an X mirror which is attached to the top stage 2 and has a reflecting surface perpendicular to the X-axis; 4, a Y mirror which is attached to the top stage 2 and has a reflecting surface perpendicular to the Y-axis; 5a, 5b, and 5c, X interferometers for measuring an X position; and 6a and 6b, Y interferometers for measuring a Y position. The X interferometers 5a, 5b, and 5c, and Y interferometers 6a and 6b are fixedly supported by a lens barrel support 10 shown in Fig. 14.

In alignment by an apparatus conventionally called an alignment apparatus, a laser beam is sent to strike each predetermined position on a reflecting mirror attached to a stage. Position variation information at the beam incident position along the beam incident direction is acquired from the reflected

beam to perform position detection. Alignment control is done based on the detection result. As a rotational direction detection means, pieces of position variation information at two beam incident positions along a single axis are obtained. More specifically, the measuring system in Fig. 13 detects positions in the X, θ , and ωY directions from pieces of position variation information based on the X interferometers 5a, 5b, and 5c and in the Y and ωX directions by the Y interferometers 6a and 6b. Alignment control along the five axes except for the Z-axis is performed based on pieces of position detection information by the laser interferometers.

Referring back to Fig. 14, reference numerals 12 denote Z displacement sensors such as linear encoders or electrostatic capacitance sensors arranged in the above-described stage. The Z displacement sensors 12 measure displacement of the top stage 2 with respect to the X stage 13 at three positions, and can measure displacements of the top stage 2 in the Z and tilt directions. The Z tilt direction of the top stage 2 with respect to the lens barrel support 10 can be measured from the measurement values of the Z displacement sensors 12 and those of three interferometers (not shown) for performing Z measurement of the stage surface plate 11 with respect to the lens barrel support 10. Z-axis alignment

control is executed based on the detection results.

Alternatively, Z measurement of the top stage 2 may be directly performed using an interferometer, as shown in Fig. 15. In Fig. 15, reference numeral 18 denotes a mirror attached to the lens barrel support 10 in order to perform Z measurement; 19, a mirror which is integrally made up of an X mirror and a Z measuring mirror which forms an acute angle with the X mirror, and reflects, to the Z direction, measurement light incident parallel to the plane in which the top stage 2 moves; and 20, a Z measuring interferometer 20. This measuring method can directly measure the Z position and displacement of the top stage 2 by using the lens barrel support 10 as a measurement reference.

The alignment apparatus using the above Z position detection means suffers the following problems.

In the prior art shown in Figs. 13 and 14, Z position information of the top stage is obtained from the positional relationship between the X stage and the top stage. A measurement error occurs due to deformation of the stage guide when the stage accelerating/decelerating inertial force or stage weight acts as a moving load, or by deformation of the surface stage or structure which supports the stage. This inhibits precise alignment.

In the prior art shown in Fig. 15, the X or Y measuring mirror and the Z measuring mirror having an

inclined surface are integrated. The X or Y measuring mirror which must exhibit high precision undergoes processing or mirror adhesion for Z measurement. This decreases the flatness and alignment precision. The
5 alignment precision also decreases owing to an increase in weight or deformation caused by changes in adhesive over time.

SUMMARY OF THE INVENTION

10 The present invention has been proposed to solve the conventional problems, and has as its object to provide an exposure apparatus including an interferometer system that enable Z-direction measurement using a lens barrel support as a
15 measurement reference and enable high-precision alignment by only mounting a bar mirror on a stage.

To solve the above problems, an exposure apparatus according to present invention, having a projection optical system for projecting a pattern
20 formed on a master onto a substrate, a stage capable of moving with respect to the projection optical system while holding at least either object of the substrate and master, and a lens barrel support which supports the projection optical system, includes: an
25 interferometer system having an interferometer for measuring a Z position and displacement of the stage with respect to the lens barrel support by using a Z

measuring mirror which is arranged on the stage and has a reflecting surface substantially parallel to an XY plane.

More specifically, the exposure apparatus

5 comprises a projection optical system for projecting a pattern formed on a master onto a substrate, a stage capable of moving with respect to the projection optical system while holding the substrate or master, and a lens barrel support which supports the projection

10 optical system, and includes an interferometer system having an interferometer for measuring the position or displacement of the stage with respect to the lens barrel support. In this arrangement, the interferometer is arranged on at least one of the X and

15 Y movable portions of the stage. Measurement light emitted by the interferometer is guided almost perpendicularly to the XY plane. The measurement light is reflected by a first reflecting surface attached to the lens barrel support toward the center of the lens

20 barrel support, and travels toward a second reflecting surface at the center.

The measurement light is reflected by the second reflecting surface to almost perpendicularly strike a bar mirror having a reflecting surface almost parallel

25 to the moving plane of the stage. Z measurement is performed by the reflected measurement light.

In the exposure apparatus, the interferometer

system desirably includes a plurality of interferometer systems arranged on the apparatus. The interferometer may be mounted on either of the stage and a movable portion which follows the stage. The stage may have an
5 elongated mirror for Z measurement which is longer in the stroke direction of the movable portion which supports the interferometer, and the elongated mirror for Z measurement may use the upper surface of either of X and Y measuring mirrors. In the interferometer
10 system, measurement light emitted by the interferometer may strike the Z measuring mirror via a plurality of mirrors or prisms attached to the lens barrel support serving as a measurement reference. Measurement light incident on the Z measuring mirror is preferably almost
15 perpendicular to the reflecting surface.

The interferometer desirably emits a total of four beams including two measurement beams and two reference beams, and the four beams are desirably formed with a cross-shaped positional relationship at
20 an almost equal interval. A mirror or prism arranged immediately in front of the Z measuring mirror desirably has at least two reflecting surfaces for reflecting measurement light to the Z measuring mirror and reference light back to the incident optical path.

25 An exposure apparatus according to the present invention comprises a Y stage movable in a Y direction; an X stage movable in an X direction with respect to

the Y stage; a Z mirror which is mounted on the X stage
or Y stage and has a reflecting surface parallel to an
XY plane; a mirror or prism for guiding to the Z mirror
a beam emitted to a Z direction by the Y stage; and an
5 interferometer for detecting a Z position of the X
stage or Y stage by using the beam reflected by the Z
mirror.

In this case, the interferometer is desirably
mounted on the Y stage or X stage. The interferometer
10 is desirably mounted in the X or Y direction, the
exposure apparatus desirably further comprises an
optical element having a reflecting surface for
reflecting a beam from the X or Y direction to the Z
direction, and the interferometer desirably emits a
15 beam parallel to the X or Y direction toward the
optical element. The mirror or prism for guiding the
beam to the Z mirror may have a first mirror or prism
for reflecting to the X or Y direction a beam emitted
to the Z direction by the Y stage or X stage, and a
20 second mirror or prism for reflecting to the Z
direction the beam reflected by the first mirror or
prism and irradiating the Z mirror with the beam. The
first mirror or prism and the second mirror or prism
are desirably elongated in the X or Y direction. The
25 second mirror or prism preferably has a reflecting
surface for reflecting to the first mirror or prism a
reference light component of the beam reflected by the

first mirror or prism.

A semiconductor device manufacturing method according to the present invention comprises the steps of installing a plurality of semiconductor

5 manufacturing apparatuses including any one of the above-described exposure apparatuses in a semiconductor manufacturing factory, and manufacturing a semiconductor device by using the plurality of semiconductor manufacturing apparatuses.

10 Preferably in the semiconductor device manufacturing method, further comprises the steps of connecting the plurality of semiconductor manufacturing apparatuses to a local area network; connecting the local area network to an external network outside the
15 semiconductor manufacturing factory; acquiring information about the exposure apparatus from a database on the external network by using the local area network and the external network; and controlling the exposure apparatus on the basis of the acquired
20 information.

Preferably in the semiconductor device manufacturing method, a database provided by a vendor or user of the exposure apparatus is accessed via the external network, thereby obtaining maintenance
25 information of the manufacturing apparatus by data communication, or data communication is performed between the semiconductor manufacturing factory and

another semiconductor manufacturing factory via the external network, thereby performing production management.

5 A semiconductor manufacturing factory according to the present invention comprises a plurality of semiconductor manufacturing apparatuses including any one of the above-described exposure apparatuses, a local area network for connecting the plurality of semiconductor manufacturing apparatuses, and a gateway
10 for connecting the local area network to an external network outside the semiconductor manufacturing factory, wherein information about at least one of the plurality of semiconductor manufacturing apparatuses can be communicated.

15 A maintenance method for an exposure apparatus installed in a semiconductor manufacturing factory according to the present invention comprises the steps of preparing a database for accumulating information about maintenance of the exposure apparatus on an
20 external network outside a factory where any one of the above-described exposure apparatuses is installed, connecting the exposure apparatus to a local area network in the factory, and maintaining the exposure apparatus on the basis of information accumulated in
25 the database by using the external network and the local area network.

Preferably, the exposure apparatus according to

the present invention further comprises an interface
for connecting a network; a computer for executing
network software for communicating maintenance
information of the exposure apparatus via the network;
5 and a display for displaying the maintenance
information of the exposure apparatus that is
communicated by the network software executed by the
computer.

Preferably, in the exposure apparatus according
10 to the present invention, the network software provides
on the display a user interface for accessing a
maintenance database which is provided by a vendor or
user of the exposure apparatus and connected to the
external network outside a factory where the exposure
15 apparatus is installed, and enables obtaining
information from the database via the external network.

Other features and advantages of the present
invention will be apparent from the following
description taken in conjunction with the accompanying
20 drawings, in which like reference characters designate
the same or similar parts throughout the figures
thereof.

BRIEF DESCRIPTION OF THE DRAWINGS

25 The accompanying drawings, which are incorporated
in and constitute a part of the specification,
illustrate embodiments of the invention and, together

with the description, serve to explain the principles of the invention.

Fig. 1 is a perspective view schematically showing Z measurement according to the first embodiment of the present invention;

Fig. 2 is a front view showing an exposure apparatus according to the embodiment of the present invention;

Fig. 3 is a perspective view schematically showing Z measurement according to the second embodiment of the present invention;

Fig. 4 is a front view showing the optical path of an interferometer according to the embodiment of the present invention;

Fig. 5 is a view showing a Z measuring mirror according to the embodiment of the present invention;

Fig. 6 is a view showing a Z measuring mirror according to the embodiment of the present invention;

Fig. 7 is a perspective view schematically showing Z measurement according to the third embodiment of the present invention;

Fig. 8 is a view showing the concept of a semiconductor device production system using the exposure apparatus according to the present invention when viewed from a given angle;

Fig. 9 is a view showing the concept of the semiconductor device production system using the

exposure apparatus according to the present invention when viewed from another angle;

Fig. 10 is a view showing an example of a user interface;

5 Fig. 11 is a flow chart for explaining the flow of a device manufacturing process;

Fig. 12 is a flow chart for explaining a wafer process;

10 Fig. 13 is a perspective view schematically showing a conventional measuring system;

Fig. 14 is a front view showing a conventional exposure apparatus; and

Fig. 15 is a front view showing another conventional exposure apparatus.

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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Preferred embodiments of the present invention will now be described in detail in accordance with the accompanying drawings.

20 <First Embodiment>

Fig. 2 is a schematic front view of a semiconductor exposure apparatus according to the first embodiment that best shows the features of the present invention.

25 In Fig. 2, reference numeral 32 denotes an illumination unit for illuminating a reticle as a master; 33, a reticle having a pattern to be

transferred; 34, a projection lens (projection optical system) for projecting a pattern formed on the reticle 33 onto a wafer serving as a substrate; 35, a lens barrel support which supports the lens 34; 36, a main body (lens barrel support) active mount which supports the lens barrel support 35 to suppress its vibrations and cuts off vibrations from the floor; and 38, an alignment surface plate which aligns the relative positions of the main body active mount 36 and a stage active mount 37 and allows installing their mounts.

Reference numerals 21 and 22 denote Z measuring mirrors fixed to the lens barrel support 35; and 39, a movable mirror for Z measurement which has two reflecting surfaces and is integrated with a Y mirror 29 (to be described later).

Reference numeral 31 denotes a Y stage movable in the Y direction; 40, an X stage movable in the X direction with respect to the Y stage 31; and 41, a stage surface plate which supports the X stage 40 and Y stage 31. The stage active mount 37 suppresses vibrations of the stage surface plate 41 caused by movement of the stage, and cuts off vibrations from the floor.

The Y stage 31 and X stage 40 are supported in a non-contact manner by the stage surface plate 41 via a hydrostatic bearing (not shown).

Reference numeral 42 denotes a driving Y linear

motor for moving the Y stage 31 in the Y direction.

The movable element of the Y linear motor 42 is

attached to the Y stage 31, and its stator element is attached onto the stage surface plate 41. The stator

5 element of the Y linear motor may be supported in a non-contact manner by the stage surface plate 41 via a hydrostatic bearing (not shown), or may be fixed to the stage surface plate 41. The exposure apparatus further comprises a driving X linear motor (not shown) for
10 moving the X stage 40 in the X direction. The movable element of the X linear motor is attached to the X stage, and its stator element is attached to the Y stage. The X linear motor generates an X-direction driving force between the Y stage 31 and the X stage 40.

15 Reference numeral 23 denotes a laser interferometer for measuring the relative position of the lens barrel support 35 and that of a substrate top stage 27, and measuring the posture of the top stage 27; and 25, a Z interferometer for measuring from the Y
20 stage 31 the distance between the lens barrel support 35 and the movable mirror 39 on the top stage 27, and calculating the Z position of the top stage 27. The top stage 27 is mounted on the X stage 40, and can be slightly moved by an actuator (not shown) with respect
25 to the X stage 40.

Reference numeral 26 denotes a wafer chuck (substrate holder) for holding a semiconductor

substrate (wafer; not shown) coated with a
photosensitive agent that is to be exposed to a pattern.
The θ Z (top stage) 27 aligns the wafer chuck 26 in the
Z, θ , ω X, and ω Y directions.

5 Fig. 1 shows the arrangement of an interferometer
system for measuring the position and displacement of
the top stage 27 by the laser interferometer 23, a
laser interferometer 24, and the laser interferometer
25 for Z measurement.

10 In Fig. 1, the wafer chuck 26 supports a wafer
(not shown). The top stage 27 supports the wafer chuck
26. The top stage 27 moves a long stroke in the X and
Y directions by a guide and actuator (neither is shown),
and moves a short stroke in the Z direction and
15 rotational directions ω X, ω Y, and θ .

Reference numeral 28 denotes an X mirror attached
to the top stage 27. The Y mirror 29 is attached to
the top stage 27. Reference numeral 30 denotes a Z
mirror integrated on the upper surface of the Y mirror
20 29. The X mirror 28 is arranged with its reflecting
surface perpendicular to the X direction. The Y mirror
29 is arranged with its reflecting surface
perpendicular to the Y direction. The Z mirror 30 is
an optical member arranged with its reflecting surface
25 parallel to the XY plane.

Reference numerals 23a, 23b, and 23c denote X
interferometers for measuring an X position. The X

interferometers 23a, 23b, and 23c send laser beams parallel to the X direction to predetermined positions on the reflecting surface of the X mirror 28. From the reflected beams, the X interferometers 23a, 23b, and
5 23c detect pieces of position variation information along the beam incident direction (X direction).

Reference numerals 24a and 24b denote Y interferometers for measuring a Y position. The Y interferometers 24a and 24b send laser beams parallel to the Y direction to
10 predetermined positions on the reflecting surface of the Y mirror 29. From the reflected beams, the Y interferometers 24a and 24b detect pieces of position variation information along the beam incident direction (Y direction).

15 The interferometers 23 and 24 are fixedly supported by a support (not shown) serving as a measurement reference. For example, the laser interferometers 23 and 24 are fixed to the lens barrel support which is a structure integrated with the
20 projection lens 34.

The Z interferometer 25 for measuring a Z position is mounted on the Y stage 31. The Z interferometer 25 is arranged to emit a beam by the Z interferometer 25 so as to be perpendicularly incident
25 on the XY plane, or is so constituted as to guide the emitted beam perpendicularly to the XY plane by an optical element such as a mirror.

The first and second mirrors 21 and 22 guide light emitted by the Z interferometer 25 to the Z mirror 30. The first and second mirrors 21 and 22 are supported and fixed so as to have a reflecting surface with an acute angle with respect to the lens barrel support 35 serving as a measurement reference or to measurement light from the Z interferometer 25. The first and second mirrors 21 and 22 are elongated mirrors long in the stroke direction (Y direction) of the movable portion (Y stage 31) on which the Z interferometer 25 is arranged.

The Z interferometer 25 is a plane mirror interferometer whose measurement light is reflected twice by the Z mirror 30 (double path). At this time, reference light is guided to the mirror 22 with a shape shown in Fig. 5 or a functionally similar beam layout shown in Fig. 5.

The first mirror 21 reflects to the second mirror 22 a beam emitted by the Z interferometer 25 perpendicularly to the XY plane. The second mirror 22 reflects reference light 44 back to the incident optical path, reflects measurement light 43 to the Z mirror 30, and reflects the measurement light reflected by the Z mirror 30 to the first mirror 21. The mirror 21 reflects the reflected reference light 44 and measurement light 43 toward the Z interferometer 25.

From the reflected light, the Z interferometer 25

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detects position variation information along the beam
incident direction (Z direction). That is, the Z
interferometer 25 detects the optical path length
change amount between the second mirror 22 and the Z
5 mirror 30 on the basis of the reference light 44 and
measurement light 43. Thus, the second mirror is
desirably fixedly supported by a support (not shown)
serving as a measurement reference for the
interferometers 23 and 24. The first mirror may be
10 fixed to the same support (not shown).

The Z mirror 30 reflects a beam from the Z
interferometer 25 back to the incident optical path.
The Z mirror 30 has a length corresponding to the
moving amount of the top stage 27 in the X direction,
15 and is fixedly supported on the top stage 27.

Each laser interferometer is a device for
detecting position change information (displacement,
speed, or the like) of the reflecting surface. Details
of the structure are well known, and a description
20 thereof will be omitted.

The positions of the mirrors 28 and 29, i.e., the
initial position of the top stage 27 is stored in a
control unit (not shown). The current position of the
top stage 27 is obtained by adding the displacements of
25 the mirrors 28 and 29 measured by the laser
interferometers to the initial position. The X- and Y
positions of the top stage 27 are measured by the

interferometers 23a and 24a. The rotational amount of the top stage 27 in the θ direction is measured by detecting the difference between the detection values of the laser interferometers 23b and 23c and performing arithmetic processing by the control unit. The rotational amount of the top stage 27 in the ωY direction is measured by performing arithmetic processing by the control units of the laser interferometers 23a and 23b. The rotational amount of the top stage 27 in the ωX direction is measured by detecting the difference between the detection values of the laser interferometers 24a and 24b (or 25a and 25b) and performing arithmetic processing within the control box.

The Z position of the top stage 27 is obtained by adding the displacement of the mirror 30 measured by the laser interferometer 25 to the initial position of the top stage 27 stored in the control unit.

As the features of the first embodiment, the mirror mounted on the top stage 27 is simple, and (1) the Y mirror 29 and Z mirror 30 can be integrated. This can downsize the shape of the top stage 27, and (2) an uncertain alignment error factor such as thermal deformation of the top stage 27 can be reduced. The top stage 27 can be reduced in weight, and (3) the natural frequency of the top stage 27 can be increased to improve the alignment control performance.

The Z measurement interferometer is mounted on the Y stage in this embodiment, but can be mounted on the X stage with the same effects.

Referring back to Fig. 2, when the operation of the semiconductor exposure apparatus according to the first embodiment starts, the main body active mount 36 moves to a prescribed position to release the lens barrel support 35 from vibrations from the floor. Then, the stage active mount 37 moves to a prescribed position to release the stage surface plate 41 from vibrations from the floor. Each laser interferometer is initialized and performs return-to-origin operation. As a result, the value of the interferometer exhibits a position relative to the projection lens 34 or lens barrel support 35. The position measuring method for the X stage 40, Y stage 31, and top stage (θ Z stage) 27 after return-to-origin operation has been described above.

The stage active mount 37 is controlled by calculating a reaction force on the stage surface plate 41 on the basis of the driving signals of the X stage 40, Y stage 31, and top stage (θ Z stage) 27 when each stage is driven. The stage active mount 37 may be subjected to feed forward control so as not to cause the stage surface plate 41 to vibrate or be displaced by the reaction force.

The reticle 33 is set on a reticle stage (not

shown). The reticle stage is supported on a reticle stage surface plate supported by the lens barrel support 35, and can move while holding the reticle bearing a circuit pattern. Exposure light which
5 illuminates the pattern of the reticle 33 on the reticle stage and illuminates a wafer on the top stage 27 is generated by the illumination unit 32.

The wafer is scanned in synchronism with the reticle 33. While the reticle stage and the wafer
10 stage which supports the wafer are scanned, their positions are continuously detected by corresponding interferometers, and fed back to the driving units of the reticle and wafer stages. This enables accurately synchronizing the scan start positions of the reticle
15 and wafer stages and controlling the scan speed within a constant-speed scan region with high precision. While the reticle and wafer stages are scanned with respect to the projection lens 34, the wafer is exposed to the reticle pattern to transfer the circuit pattern.

20 According to the first embodiment, a beam emitted from a position on the movable portion perpendicularly to the XY plane passes through the mirrors 21 and 22 attached to the lens barrel support 35, and enters the reflecting surface of the Z mirror 30 attached onto the
25 top stage 27 that is parallel to the XY plane, thereby measuring the Z position of the top stage 27. The Z position of the top stage 27 can be directly measured

by using as a reference the lens barrel support 35 which supports the interferometer. Even if the stage guides deforms or the stage surface plate which supports the stage deforms along with movement of the stage, a position measurement error in the Z direction can be reduced.

Installation of an alignment apparatus having this measuring system in a semiconductor exposure apparatus allows measuring the position and posture of a wafer at high precision, and can realize high-precision exposure.

<Second Embodiment>

The second embodiment of the present invention will be described below with reference to the accompanying drawings.

Fig. 3 shows an alignment apparatus constituted by mounting two interferometer systems for Z measurement shown in Fig. 1 in an exposure apparatus shown in Fig. 2.

In Fig. 3, a lens barrel which shields measurement light is positioned at the center of a lens barrel support 35 in the exposure apparatus as shown in Fig. 2. Thus, the Z interferometer system as described in the first embodiment cannot perform Z measurement in the entire stroke range when the movable range of the top stage 27 is a long stroke. To solve this problem, the second embodiment adopts two Z interferometer

systems. Reference numerals 25a and 25b denote Z
interferometers which measure a Z position and are
mounted on a Y stage 31. Beams emitted by the Z
interferometers 25a and 25b are arranged such that
5 beams emitted by the Z interferometers 25a and 25b
travel perpendicularly to the XY plane, or are so
constituted as to vertically guide the beams by optical
elements such as mirrors. Emerging beams pass through
mirrors 21 and 22 attached to the lens barrel support
10 35, and strike the reflecting surfaces of Z mirrors 30
attached onto a top stage 27 that are parallel to the
XY plane, thereby measuring the Z position of the top
stage 27.

At this time, the reflecting surfaces of the two
15 Z mirrors 30 are measured by switching the operation
status (e.g., stage position) of the top stage 27. The
top stage 27 can be measured while measurement light
avoids not only the lens barrel but also an obstacle
which shields it.

20 In switching, a control unit (not shown) hands
over a measurement value from an interferometer which
has performed measurement to an interferometer which
can start measurement. In switching, the stage is
positioned in a measurement region where two
25 interferometer systems overlap each other. The
overlapping measurement region is designed in
consideration of the switching time so as to enable

switching even while the stage moves.

Each of the Z interferometers 25a and 25b used for Z measurement emits two, measurement and reference beams. As for the emerging beams, measurement light 43 and reference light 44 which are emitted by the Z interferometer 25 perpendicularly to the XY plane are reflected by the first mirror 21 and guided to the second mirror 22, as shown in Fig. 4. The reference light 44 is reflected back to the incident optical path by the second mirror 22, whereas the measurement light 43 enters the reflecting surface of the Z mirror 30. Position variation information along the beam incident direction (Z direction) is detected from the reflected light.

The second mirror 22 has a shape shown in Fig. 6 or a functionally similar shape, and provides a cross-shaped positional relationship between the measurement light 43 and reference light 44 which enter the mirror 22, as shown in Fig. 6. If the beam interval from the intersection is equal between beams connected by lines, an optical path difference by a tilt of the mirror or the like does not occur and does not cause any measurement error. From this, the beam interval from the center is desirably equal.

In the use of the interferometer system as described in the second embodiment, the optical paths of reference light and measurement light are almost

equal up to a position (second mirror 22) immediately above the top stage 27 to be measured even if the optical path (dead path) of measurement light is long, like the second embodiment. Measurement is hardly
5 influenced by air turbulence or the like, and is higher in precision than a conventional measuring method.

In the embodiment, only one or two interferometer systems are arranged on the top stage 27. The present invention is not limited to this, and three or more
10 interferometer systems may be arranged. Measurement at three portions can attain not only Z displacement of the top stage 27 using the lens barrel support 35 as a reference but also rotational information of the top stage 27 in the tilt direction (ωX and ωY).

15 Beams emitted by the Z interferometers 25a and 25b and a beam incident on the Z mirror 30 need not be perpendicular to the XY plane, and the incident beam may be tilted.

The measuring method, measuring apparatus, or
20 interferometer system according to the embodiment can be applied not only to an exposure apparatus but also to an alignment apparatus for another precision processing machine. When the measuring method, measuring apparatus, or interferometer system is used
25 in the exposure apparatus, it is not limited to the above-described wafer stage but can also be applied to a reticle stage. Further, the same effects can also be

obtained by applying the measuring system of the embodiment to an alignment apparatus for accurately aligning a moving member.

<Third Embodiment>

5 The third embodiment of the present invention will be described below with reference to the accompanying drawings.

Fig. 7 shows an alignment apparatus having an optical element 51 for reflecting, to the Z direction, measurement light and reference light incoming from the Y direction, instead of the Z interferometer 25 mounted on the Y stage 31 in the above embodiment.

In Fig. 7, the same reference numerals as in the above embodiments denote the same parts, and a description thereof will be omitted.

The optical element 51 has a reflecting surface for reflecting, to the Z direction, measurement light and reference light incoming from the Y direction. Reference numeral 52 denotes a Z interferometer for measuring a Z position. The Z interferometer 52 emits measurement light and reference light parallel to the Y direction toward the optical element 51. The optical element 51 reflects the measurement light and reference light emitted by the Z interferometer 52 toward a first mirror 21 in the Z direction. The light reflected by the first mirror 21, a second mirror 22, and a Z mirror 30 is reflected by the optical element 51 toward the

interferometer 52. The interferometer 52 detects position change information along the Z direction from the reflected light.

The third embodiment can eliminate the need for mounting an interferometer on a Y stage 31, and can implement a lightweight, high-rigidity Y stage. Since the Y stage 31 which supports the optical element 51 moves in the Y direction, the optical element 51 need not be an elongated mirror, unlike the mirrors 21, 22, and 28 to 30.

The third embodiment can attain the same effects as those of the above embodiments.

<Embodiment of Semiconductor Production System>

A production system for a semiconductor device (semiconductor chip such as an IC or LSI, liquid crystal panel, CCD, thin-film magnetic head, micromachine, or the like) by the exposure apparatus according to the present invention will be exemplified. A trouble remedy or periodic maintenance of a manufacturing apparatus installed in a semiconductor manufacturing factory, or maintenance service such as software distribution is performed by using a computer network outside the manufacturing factory.

Fig. 8 shows the overall system cut out at a given angle. In Fig. 8, reference numeral 101 denotes a business office of a vendor (apparatus supply manufacturer) which provides a semiconductor device

manufacturing apparatus. Examples of the manufacturing apparatus are semiconductor manufacturing apparatuses for various processes used in a semiconductor manufacturing factory, such as pre-process apparatuses (lithography apparatus including an exposure apparatus, resist processing apparatus, and etching apparatus, annealing apparatus, film formation apparatus, planarization apparatus, and the like) and post-process apparatuses (assembly apparatus, inspection apparatus, and the like). The business office 101 comprises a host management system 108 for providing a maintenance database for the manufacturing apparatus, a plurality of operation terminal computers 110, and a LAN (Local Area Network) 109 which connects the host management system 108 and computers 110 to build an intranet. The host management system 108 has a gateway for connecting the LAN 109 to Internet 105 as an external network of the business office, and a security function for limiting external accesses.

Reference numerals 102 to 104 denote manufacturing factories of the semiconductor manufacturer as users of manufacturing apparatuses. The manufacturing factories 102 to 104 may belong to different manufacturers or the same manufacturer (pre-process factory, post-process factory, and the like). Each of the factories 102 to 104 is equipped with a plurality of manufacturing apparatuses 106, a

LAN (Local Area Network) 111 which connects these
apparatuses 106 to construct an intranet, and a host
management system 107 serving as a monitoring apparatus
for monitoring the operation status of each
5 manufacturing apparatus 106. The host management
system 107 in each of the factories 102 to 104 has a
gateway for connecting the LAN 111 in the factory to
the Internet 105 as an external network of the factory.
Each factory can access the host management system 108
10 of the vendor 101 from the LAN 111 via the Internet 105.
The security function of the host management system 108
authorizes access of only a limited user. More
specifically, the factory notifies the vendor via the
Internet 105 of status information (e.g., the symptom
15 of a manufacturing apparatus in trouble) representing
the operation status of each manufacturing apparatus
106, and receives response information (e.g.,
information designating a remedy against the trouble,
or remedy software or data) corresponding to the
20 notification, or maintenance information such as the
latest software or help information. Data
communication between the factories 102 to 104 and the
vendor 101 and data communication via the LAN 111 in
each factory adopt a communication protocol (TCP/IP)
25 generally used in the Internet. Instead of using the
Internet as an external network of the factory, a
dedicated network (e.g., ISDN) having high security

which inhibits access of a third party can be adopted. Also the user may construct a database in addition to the one provided by the vendor and set the database on an external network, and the host management system may
5 authorize access to the database from a plurality of user factories.

Fig. 9 is a view showing the concept of the overall system of this embodiment that is cut out at a different angle from Fig. 8. In the above example, a
10 plurality of user factories having manufacturing apparatuses and the management system of the manufacturing apparatus vendor are connected via an external network, and production management of each factory or information of at least one manufacturing
15 apparatus is communicated via the external network. In the example of Fig. 9, a factory having manufacturing apparatuses of a plurality of vendors and the management systems of the vendors for these manufacturing apparatuses are connected via the
20 external network of the factory, and maintenance information of each manufacturing apparatus is communicated. In Fig. 9, reference numeral 201 denotes a manufacturing factory of a manufacturing apparatus user (semiconductor device manufacturer) where
25 manufacturing apparatuses for various processes, e.g., an exposure apparatus 202, resist processing apparatus 203, and film formation apparatus 204 are installed in

the manufacturing line of the factory. Fig. 9 shows only one manufacturing factory 201, but a plurality of factories are networked in practice. The respective apparatuses in the factory are connected to a LAN 206 to build an intranet, and a host management system 205 manages the operation of the manufacturing line. The business offices of vendors (apparatus supply manufacturers) such as an exposure apparatus manufacturer 210, resist processing apparatus manufacturer 220, and film formation apparatus manufacturer 230 comprise host management systems 211, 221, and 231 for executing remote maintenance for the supplied apparatuses. Each host management system has a maintenance database and a gateway for an external network, as described above. The host management system 205 for managing the apparatuses in the manufacturing factory of the user, and the management systems 211, 221, and 231 of the vendors for the respective apparatuses are connected via the Internet or dedicated network serving as an external network 200. If a trouble occurs in any one of a series of manufacturing apparatuses along the manufacturing line in this system, the operation of the manufacturing line stops. This trouble can be quickly solved by remote maintenance from the vendor of the apparatus in trouble via the Internet 200. This can minimize the stop of the manufacturing line.

Each manufacturing apparatus in the semiconductor manufacturing factory comprises a display, a network interface, and a computer for executing network access software and apparatus operating software which are

5 stored in a storage device. The storage device is a built-in memory, hard disk, or network file server. The network access software includes a dedicated or general-purpose web browser, and provides a user interface having a window as shown in Fig. 10 on the

10 display. While referring to this window, the operator who manages manufacturing apparatuses in each factory inputs, in input items on the windows, pieces of information such as the type of manufacturing apparatus (401), serial number (402), subject of trouble (403),

15 occurrence date (404), degree of urgency (405), symptom (406), remedy (407), and progress (408). The pieces of input information are transmitted to the maintenance database via the Internet, and appropriate maintenance information is sent back from the maintenance database

20 and displayed on the display. The user interface provided by the web browser realizes hyperlink functions (410 to 412), as shown in Fig. 10. This allows the operator to access detailed information of each item, receive the latest-version software to be

25 used for a manufacturing apparatus from a software library provided by a vendor, and receive an operation guide (help information) as a reference for the

operator in the factory. Maintenance information provided by the maintenance database also includes information concerning the features of the above-described embodiments. The software library also provides the latest software for implementing the features of the above-described embodiments.

A semiconductor device manufacturing process using the above-described production system will be explained. Fig. 11 shows the flow of the whole manufacturing process of the semiconductor device. In step 1 (circuit design), a semiconductor device circuit is designed. In step 2 (mask formation), a mask having the designed circuit pattern is formed. In step 3 (wafer formation), a wafer is formed by using a material such as silicon. In step 4 (wafer process) called a pre-process, an actual circuit is formed on the wafer by lithography using the prepared mask and wafer. Step 5 (assembly) called a post-process is the step of forming a semiconductor chip by using the wafer formed in step 4, and includes an assembly process (dicing and bonding) and packaging process (chip encapsulation). In step 6 (inspection), the semiconductor device manufactured in step 5 undergoes inspections such as an operation confirmation test and durability test. After these steps, the semiconductor device is completed and shipped (step 7). For example, the pre-process and post-process are performed in

separate dedicated factories, and each of the factories receives maintenance by the above-described remote maintenance system. Information for production management and apparatus maintenance is communicated
5 between the pre-process factory and the post-process factory via the Internet or dedicated network.

Fig. 12 shows the detailed flow of the wafer process. In step 11 (oxidation), the wafer surface is oxidized. In step 12 (CVD), an insulating film is
10 formed on the wafer surface. In step 13 (electrode formation), an electrode is formed on the wafer by vapor deposition. In step 14 (ion implantation), ions are implanted in the wafer. In step 15 (resist processing), a photosensitive agent is applied to the
15 wafer. In step 16 (exposure), the above-mentioned exposure apparatus exposes the wafer to the circuit pattern of a mask. In step 17 (developing), the exposed wafer is developed. In step 18 (etching), the resist is etched except for the developed resist image.
20 In step 19 (resist removal), an unnecessary resist after etching is removed. These steps are repeated to form multiple circuit patterns on the wafer. A manufacturing apparatus used in each step undergoes maintenance by the remote maintenance system, which
25 prevents a trouble in advance. Even if a trouble occurs, the manufacturing apparatus can be quickly recovered. The productivity of the semiconductor

device can be increased in comparison with the prior art.

The present invention can more accurately measure the position of a stage apparatus used in a
5 semiconductor exposure apparatus or the like, and can achieve higher-precision alignment control.

As many apparently widely different embodiments of the present invention can be made without departing from the spirit and scope thereof, it is to be
10 understood that the invention is not limited to the specific embodiments thereof except as defined in the claims.